



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: YU-CHUAN CHANG AND XUE-NING REN

Serial No: 09/262,960 Attorney Docket No: TD9907IN

Filed: 03/04/99 Group Art Unit: 2133

For: METHOD AND MODULE FOR UNIVERSAL  
TEST OF COMMUNICATION PORTS Examiner: CHUNG, P

**AMENDMENT RESPONSIVE TO OFFICE ACTION**

**MAILED 05/22/02**

BOX AF

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

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JUL 18 2005

OFFICE OF PETITIONS



022192

PATENT AND TRADEMARK OFFICE

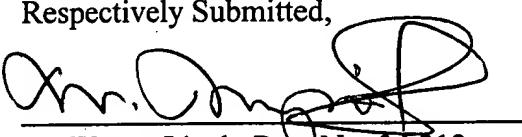
Transmitted herewith for filing is Applicant's Response to Office Action. Should the Examiner have any question, he or she is invited to call or fax the undersigned at the telephone numbers indicated below. If, for any reason, this Response is not timely filed, this is a petition for extension of time as necessary for timely filing, and the Commissioner is authorized to charge any necessary statutory fees to Charge Account No. 50-1260.

Respectively Submitted,

7/23/02

Date

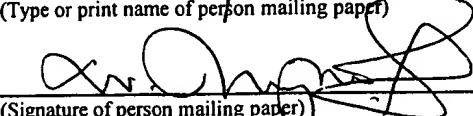
PTO Customer No. 022192

  
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CERTIFICATE OF MAILING (37 CFR §1.8a)

I hereby certify that this paper (along with any documents referred as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelop addressed to: The Commissioner of Patents and Trademarks, Washington, D.C., 20231.

W. Wayne Liauh  
(Type or print name of person mailing paper)

  
(Signature of person mailing paper)

Date

7/23/02



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**AMENDMENT****OFFICE OF PETITIONS**

In response to the Office Action mailed on 05/22/02, please amend the above-identified application as follows:

**In the Claims:**

Please amend the Claim(s) as follows:

**Claim 1 (twice amended):**

1. A universal testing module, capable of connecting to a computer having communication ports to be tested and forming communication paths through said ports, for testing the condition of each pin of a parallel port and a serial port of said communication ports, [at least] said universal testing module comprising: